

Amendments to the Claims:

This listing of claims will replace all prior version, and listings, of claims in the application:

Listing of Claims:

1-8. (Canceled).

9. (Previously Presented) An interconnect structure, comprising:

a substrate having a surface;

a dielectric layer disposed on the surface of the substrate;

a first metal line disposed in the dielectric layer, having a first and second end,
wherein the direction extending from the first end to the second end is
parallel to the substrate surface;

a second metal line disposed on the dielectric layer, wherein the second metal line
is separated from the first metal layer by the dielectric layer; and

a plurality of conductive plugs arranged along the first metal line, disposed in the
dielectric layer and on the first end of the first metal line to electrically
connect the second metal line,

wherein the first metal line and the second metal line ~~layer~~ are a gate metal layer
and a source/drain metal layer of a TFT array respectively.

10. (Original) The interconnect structure as claimed in claim 9, wherein the
substrate is a TFT-array substrate for an LCD panel.

11. (canceled).

12. (Original) The interconnect structure as claimed in claim 9, wherein the number of conductive plugs is from 2 to 5.

13. (Previously presented) The interconnect structure as claimed in claim 9, wherein the conductive plugs disposed on the first end of the first metal line electrically connect one end of the second metal line.

14. (Previously presented) An interconnect structure, comprising:
a substrate having a surface;
a dielectric layer disposed on the surface of the substrate;
a first metal line disposed in the dielectric layer, having a first and second end,
wherein the direction extending from the first end to the second end is
parallel to the substrate surface;;
a second metal line disposed on the dielectric layer; and
a plurality of plugs arranged along the first metal line, disposed on the first end of
the first metal line, wherein at least one of the plugs farther from the first end
of the metal line is conductive, and electrically connects the second metal
line.

15. (Previously presented) The interconnect structure as claimed in claim 14, wherein the substrate is a TFT-array substrate for an LCD panel.

16. (Previously presented) The interconnect structure as claimed in claim 14, wherein the number of plugs is from 2 to 5.

17. (Previously presented) The interconnect structure as claimed in claim 14, wherein the conductive plug electrically connects one end of the second metal line.

18-19. (Canceled).

20. (Previously presented) An interconnect structure, comprising:

a substrate;

a first dielectric layer disposed on the substrate;

a first metal line disposed in the first dielectric layer, having a first and second end,
wherein the direction extending from the first end to the second end is
parallel to the substrate surface;

a first plug and a second plug disposed on the first end of the first metal line,
wherein the first plug is closer to the first end than the second plug;

a second metal line disposed on the first dielectric layer, connecting the first metal
line through the second plug; and

a second dielectric layer disposed on the first dielectric layer and the second metal
line, filling the first plug.

21. (Canceled).